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F Corno, E Sanchez, MS Reorda, G Squillero - Journal of Electronic Testing, 2004 - Springer
 ... local optimizations or running **regression** tests ... each possible instruction with the syntactically correct **operands**. ... by a unique name; the **test**- program generator ...

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Enhancing a Modula-2 compiler to help students learn interactively within the Ceilidh system

GPC Mulley, K Verheyden - Knowledge Transfer, 1997 - floppsie.comp.glam.ac.uk
 ... applied to the compiler, a **regression test** suite is ... errors should show the corresponding **fragments** of source ... The conditional branch **operands** are examined for ...

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... used for checking local optimizations and running **regression** tests ... to an assembly line in the **test** program ... unsigned sum with three registers as **operands**) and the ...

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... functions for generating GIMPLE statements and **testing** whether a ... flow graph for this code **fragment**, the compiler ... is then considered a virtual **operand** using the ...

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Porting lcc to the PLX architecture

W Qin - palms.ee.princeton.edu

... For the C program **fragment** below, a = PSUBAVG_1 ... During a **regression test**, the

benchmarks will be compiled ... load operation's destination **operand** does not ...

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The GNU 64-bit PL 8 compiler: Toward an open standard environment for firmware development - all 6 versions »

W Gellerich, T Hendel, R Land, H Lehmann, M ... - IBM Journal of Research and Development, 2004 - research.ibm.com

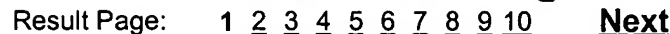
... where the %i are replaced by the specific **operand** i our validation effort covers

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A Aharon, D Goodman, M Levinger, Y Lichtenstein, Y ... - Proceedings of the 32nd ACM/IEEE conference on **Design** ..., 1995 - portal.acm.org
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J Shen, J Abraham, D Baker, T Hurson, M Kinkade - Proceedings 36 thDesign Automation Conference, 1999 - [doi.ieeecomputersociety.org](#)
 ... We applied the **regression** test suite to the chip on a tester and ... method at the RTL level during pre-silicon **verification**, and detected **design** errors in ...
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M Monachino - **Design** Automation, 1982. 19th Conference on, 1982 - [ieeexplore.ieee.org](#)
 ... DVS is that it requires no test pattern to analyze the **design**. ... **VERIFICATION** EXPERIENCE ...
 3081 testing philosophy was to use all tests for **regression** testing at ...
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C Pixley, NR Strader, WC Bruce, J Park, M Kaufmann ... - Proceedings of the International Test Conference (ITC'96) - [doi.ieeecomputersociety.org](#)
 ... Based upon these results, the **regression** suite was extended ... nificantly improve pre-tape-out **verification** of complex ... hardware proto- type of the **design** is the ...
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RB Jones, WOL John - IEEE} **Design** {\&} Test of Computers} - [comlab.ox.ac.uk](#)
 ... debugged circuit API; and a set of scalar test vectors, which are saved to support **regression** testing. ... Formal **Verification** 20 IEEE **Design** & Test of Computers ...
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... Section 4.4 describes this aspect of the **verification**. ... change from week to week in a **design** project) supports our requirements of **regression** and reuse ...

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W Anderson - Digital Technical Journal, 1992 - source.rfc822.org
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